

Serial No. 09/458,322

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B1
Each server module buffer is sized to hold, illustratively, one extent for each of the subscribers served by the respective server module. Thus, in the case of a server module supporting 500 subscribers, the corresponding server module buffer is sized to handle 500 extents (e.g., 500 service periods--1,000 seconds).

Please replace the paragraph beginning on page 10, line 8, with the following:

B2
In the exemplary embodiment, the data signal DATA received from the LAN/WAN 116 comprises internet protocol (IP) packets, while the content data provided to the switch 230 via the server modules 220 is illustratively formatted according to a high-speed version of the structures defined by the Digital Video Broadcasting-Asynchronous Serial Interface (DVB-ASI). The high-speed DVB-ASI (HS-ASI) data structures are described in more detail in commonly assigned (Attorney Docket No. 048), which is incorporated herein by reference in its entirety. Briefly, the HS-ASI data structures or packet format comprises a header portion and a payload portion. The header portion includes destination information and other information useful in routing HS-ASI packets within a network environment, illustratively a ring network or star network provided with high speed optical transmission media, such as described in commonly assigned, simultaneously filed U.S. Patent Application No. 09/458,339.

Please replace the paragraph beginning on page 13, line 27, with the following:

B3
The switch module 234 receives content data from the service modules SM₁ through SM_m via respective input ports 1 through m. The content packets forming the content extents received from the server modules 220 are multiplexed to form an output stream which is coupled to the transport processor 150 via the signal path OUT. Additionally, the switch module 234 receives a switch control signal SWC from the switch controller 236 via a control input port C. The switch controller 236 is used to control which input port 1 through m is coupled to the output port O such that the